ML4835*

Compact Fluorescent Electronic Dimming Ballast Controller

GENERAL DESCRIPTION

The ML4835 is a complete solution for a dimmable or a non-dimmable, high power factor, high efficiency electronic ballast especially tailored for a compact fluorescent lamp (CFL). The Bi-CMOS ML4835 contains controllers for "boost" type power factor correction as well as for a dimming ballast with end-of-lamp life detection.

The PFC circuits uses a new , simple PFC topology which requires only one loop for compensation. In addition, this PFC can be used with either peak- or average-current mode. This system produces a power factor of better than 0.99 with low input current THD.

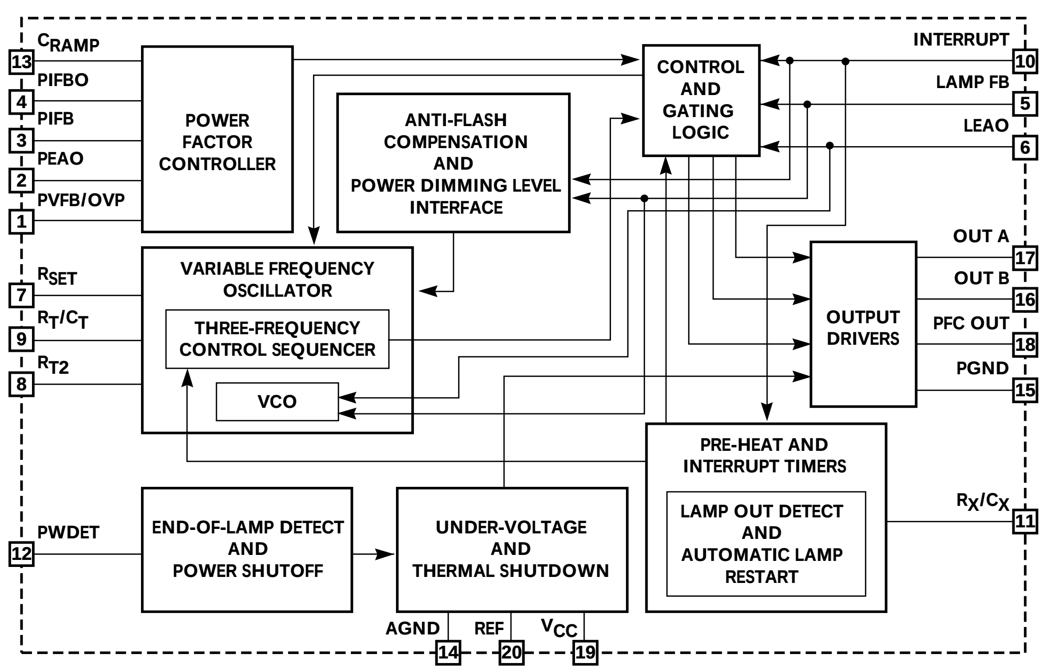
The ballast controller section provides for programmable starting sequence with individual adjustable preheat and lamp out-of-socket interrupt times. The ML4835 provides a shut down for both PFC and ballast controllers in the event of end-of-life for the CFL.

FEATURES

- Power detect for end-of-lamp-life detection
- Low distortion , high efficiency continuous boost, peak or average current sensing PFC section
- Leading- and trailing-edge synchronization between PFC and ballast
- One to one frequency operation between PFC and ballast
- Programmable start scenario for rapid/instant start lamps
- Triple frequency control network for dimming or starting to handle various lamp sizes
- Programmable restart for lamp out condition to reduce ballast heating.
- Internal over-temperature shutdown
- PFC over-voltage comparator eliminates output "runaway" due to load removal
- Low start-up current; < 0.55mA

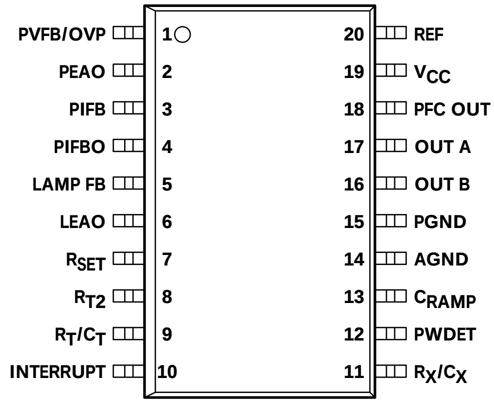
(* Indicates Part is End Of Life as of July 1, 2000)

BLOCK DIAGRAM



PIN CONFIGURATION

ML4835
20-Pin SOIC (S20)
20-Pin DIP (P20)



PIN DESCRIPTION

PIN	NAME	FUNCTION
1	PVFB/OVP	Inverting input to the PFC error amplifier and OVP comparator input.
2	PEAO	PFC error amplifier output and compensation node
3	PIFB	Senses the inductor current and peak current sense point of the PFC cycle by cycle current limit
4	PIFBO	Output of the current sense amplifier. Placing a capacitor to ground will average the inductor current.
5	LAMP FB	Inverting input of the lamp error amplifier, used to sense and regulate lamp arc current. Also the input node for dimmable control.
6	LEAO	Output of the lamp current error transconductance amplifier used for lamp current loop compensation
7	R_{SET}	External resistor which SETS oscillator F_{MAX}, and R_X/C_X charging current
8	R_{T2}	Oscillator timing component to set start frequency
9	R_T/C_T	Oscillator timing components
10	INTERRUPT	Input used for lamp-out detection and restart. A voltage less than 1V will reset the IC and cause a restart after a programmable interval.
11	R_X/C_X	Sets the timing for preheat and interrupt.
12	PWDET	Lamp output power detection
13	C_{RAMP}	Integrated voltage of the error amplifier out
14	AGND	Analog ground
15	PGND	Power ground.
16	OUT B	Ballast MOSFET driver output
17	OUT A	Ballast MOSFET driver output
18	PFC OUT	Power factor MOSFET driver output
19	V_{CC}	Positive supply voltage
20	REF	Buffered output for the 7.5V reference

ABSOLUTE MAXIMUM RATINGS

Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

Supply Current (I_{CC}) 65mA
Output Current, Source or Sink
(OUT A, OUT B, PFC OUT) DC 250mA
PIFB Input Voltage ....–3V to 2V
Maximum Forced Voltage
(PEAO, LEAO) ....–0.3V to 7.7V
Maximum Forced Current
(PEAO, LEAO) .... ±20mA

Junction Temperature 150ºC
Storage Temperature Range .... –65ºC to 150ºC
Lead Temperature (Soldering, 10 sec) 260ºC
Thermal Resistance (θ_{JA})
ML4835CP 65ºC/W
ML4835CS 80ºC/W

OPERATING CONDITIONS

Temperature Range .... 0°C to 85°C

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, $V_{CC} = V_{CCZ}$ –0.5V, R_{SET} = 11.8kΩ, R_T = 15.4kΩ, R_{T2} = 67.5kΩ, C_T = 1.5nF, T_A = Operating Temperature Range (Note 1)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
LAMP CURRENT AMPLIFIER (LAMP FB, LEAO)						
	Input Bias Current			-0.3	-1.0	µA
	Small Signal Transconductance		35	75	105	µ℧
	Input Bias Voltage		-0.3		5.0	V
	Output Low	LAMP FB = 3V, $R_L = \infty$		0.2	0.4	V
	Output High	LAMP FB = 2V, $R_L = \infty$	7.1	7.5		V
	Source Current	LAMP FB = 0V, LEAO = 6V	-80	-220		µA
	Sink Current	LAMP FB = 5V, LEAO = 0.3V	80	220		µA
PFC VOLTAGE FEEDBACK AMPLIFIER (PEAO, PVFB/OVP)						
	Input Bias Current			-0.3	-1.0	µA
	Small Signal Transconductance		35	75	105	µ℧
	Input Bias Voltage		-0.3		5.0	V
	Output Low	PVFB = 3V, $R_L = \infty$		0.2	0.4	V
	Output High	PVFB = 2V, $R_L = \infty$	6.4	6.8		V
	Source Current	PVFB = 0V, PEAO = 6V	-80	220		µA
	Sink Current	PVFB = 5V, PEAO = 0.3V	80	220		µA
PFC CURRENT-LIMIT COMPARATOR (PIFB)						
	Current-Limit Threshold		-0.9	-1.0	-1.1	V
	Propagation Delay	100mV Step and 100mV Overdrive		100		ns
PFC OVP COMPARATOR						
	OVP Threshold		2.65	2.75	2.85	V
	Hysteresis		0.14	0.20	0.30	V
	Propagation Delay			1.4		µs

ELECTRICAL CHARACTERISTICS (Continued)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
OSCILLATOR						
	Initial Accuracy (F_{MIN})	T_A = 25ºC	39.2	40	40.8	kHz
	Voltage Stability (F_{MIN})	V_{CCZ} – 4V < V_{CC} < V_{CCZ} – 0.5V		0.3		%
	Temperature Stability (F_{MIN})			0.3		%
	Total Variation (F_{MIN})	Line, Temperature	39.2		40.8	kHz
	Initial Accuracy (START)	T_A = 25ºC	49	50	51	kHz
	Voltage Stability (START)			0.3		%
	Temparature Stability (START)			0.3		%
	Total Variation (START)	Line, Temperature	49		51	kHz
	Ramp Valley to Peak			2.5		V
	Initial Accuracy (Preheat)	TA = 25ºC	60.8	64	67.2	kHz
	Total Variation (Preheat)	Line, Temperature	60.8	64	67.2	kHz
	C_T Discharge Current	V_{RTCT} = 2.5V	6.0	7.5	9.0	mA
	Output Drive Deadtime	C_T = 1.5nF		0.7		us
REFERENCE BUFFER						
	Output Voltage	T_A = 25ºC, I_O = 0mA	7.4	7.5	7.6	V
	Line Regulation	V_{CCZ} – 4V < V_{CC} < V_{CCZ} – 0.5V		10	25	mV
	Load Regulation	1mA < I_O < 10mA		2	15	mV
	Temperature Stability			0.4		%
	Total Variation	Line, Load, Temperature	7.35		7.65	V
	Long Term Stabilty	Tj=125ºC, 1000 hrs		5		mV
	Short Circuit Current			40		mA
	R_{SET} Voltage		2.4	2.5	2.6	V

ELECTRICAL CHARACTERISTICS (Continued)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
PREHEAT AND INTERRUPT TIMER (R_X = 346kΩ, C_X = 10µF)						
	Initial Preheat Period			0.86		s
	Subsequenct Preheat Period			0.72		s
	Interrupt Period			5.9		s
	R_X/C_X Charging Current		-50	-54	-58	µA
	R_X/C_X Open Circuit Voltage		0.4	0.7	1.0	V
	R_X/C_X Maximum Voltage		7.0	7.3	7.8	V
	Preheat Lower Threshold		1.6	1.75	1.9	V
	Preheat Upper Threshold		4.4	4.65	4.9	V
	Start Period End Threshold		6.2	6.6	6.9	V
	Interrupt Disable Threshold		1.1	1.25	1.4	V
	Hysteresis		0.16	0.26	0.36	V
	Input Bias Current				1	µA
POWER SHUTDOWN						
	Power Shutdown Voltage		0.9	1	1.1	V
OUTPUTS (OUT A, OUT B, PFC OUT)						
	Output Voltage Low	I_{OUT} = 20mA		0.1	0.2	V
		I_{OUT} = 200mA		1.0	2.0	V
	Output Voltage High	I_{OUT} = 20mA	V_{CC}-0.2	V_{CC}-0.1		V
	Output Voltage High	I_{OUT} = 200mA	V_{CC}-2.0	V_{CC}-1.0		V
	Output Voltage Low in UVLO	I_{OUT} = 10mA, $V_{CC} < V_{CC\ START}$			0.2	V
	Output Rise and Fall Time	CL=1000pF		50		ns
UNDER VOLTAGE LOCKOUT AND BIAS CIRCUITS						
	IC Shunt Voltage (V_{CCZ})	ICC=15mA	14.0	14.8	15.5	V
	Start-up Threshold ($V_{CC\ START}$)		V_{CCZ}-1.5	V_{CCZ}-1.0	V_{CCZ}-0.5	V
	Hysteresis		3.0	3.7	4.4	V
	Start-up Current	$V_{CC\ START}$ –0.2V		350	550	µA
	Interrupt Current	(V_{CC}–0.5V), INTERRUPT = 0V		500	750	µA
	Operating Current	(V_{CC}–0.5V)		5.5	8.0	mA
	Shutdown Temperature			130		ºC
	Hysteresis			30		ºC

Note 1: Limits are guaranteed by 100% testing, sampling, or correlation with worst case test conditions.

FUNCTIONAL DESCRIPTION

The ML4835 consists of peak or average current controlled continuous boost power factor front end section with a flexible ballast control section. Start-up and lamp-out retry timing are controlled by the selection of external timing components, allowing for control of a wide variety of different lamp types. The ballast section controls the lamp power using frequency modulation (FM) with additional programmability provided to adjust the VCO frequency range. This allows for the IC to be used with a variety of different output networks. Figure 1 depicts a detailed block diagram of ML4835.

The ML4835 provides several safety features. See the corresponding sections for more details:

- End-of-lamp life detection to detect EOL and shut-off lamps; See End Of Life Section.
- Thermal shutdown for temperature sensing extremes; See IC Bias, Under-Voltage Lockout and Thermal Shutdown Section.
- Relamping starting with anti-flash for programmable restart for lamp out conditions while minimizing “flashing” when powering from full power to dimming levels; See Starting, Re-Start, Preheat and Interrupt Section

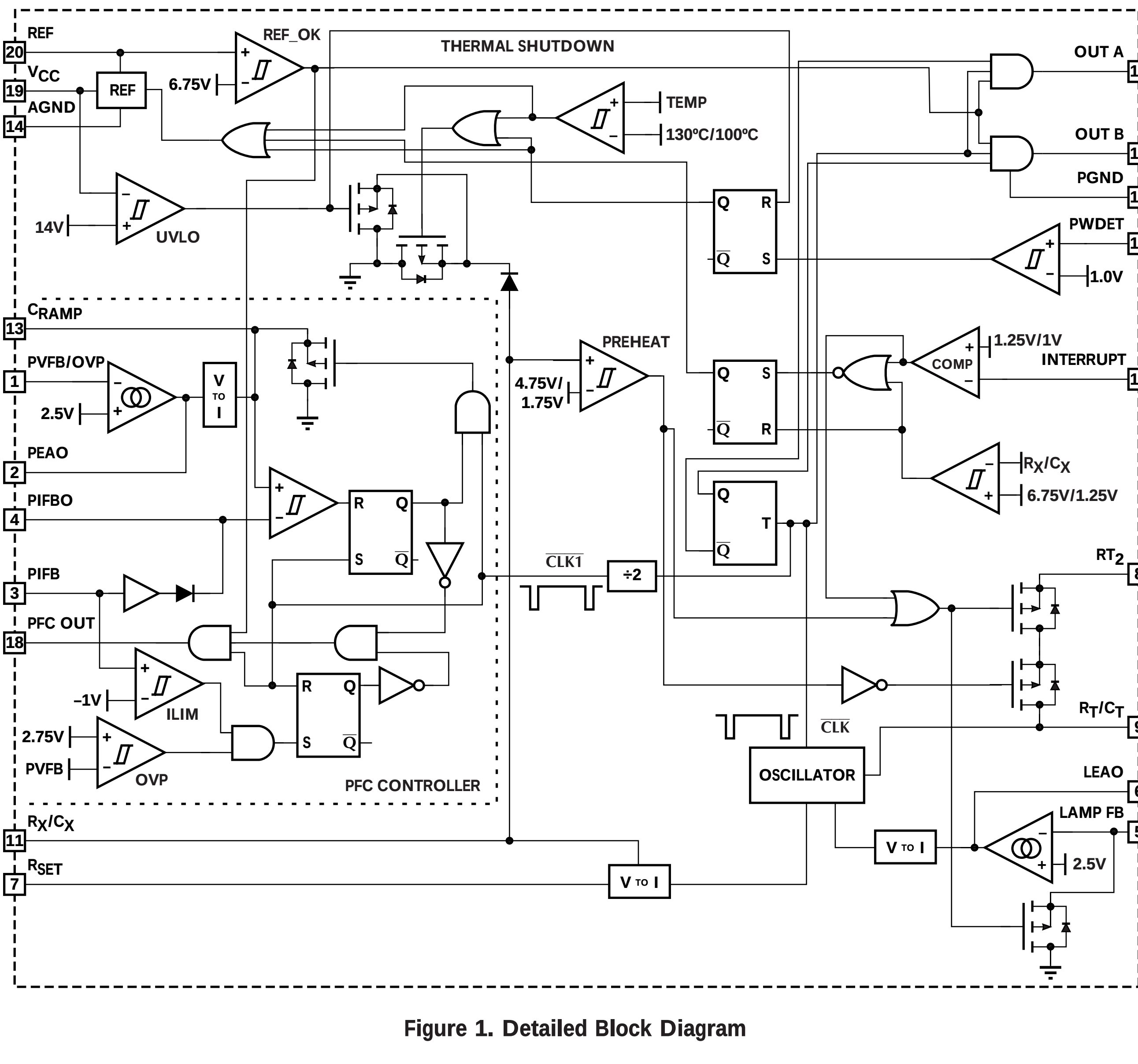


Figure 1. Detailed Block Diagram

FUNCTIONAL DESCRIPTION (Continued)

The ML4835 implements a triple frequency operation scheme: programmable three-frequency sequence for pre-heat, ignition, and dimming, that extends lamp life, simplifies lamp network design, and starts lamps at any dimming level without flashing. This addresses the need for a high-Q network for starting sequence and low-Q network for operation, minimizing parasitic losses and improving overall power efficiency. The values for the pre-heat, start, operation, and restart can be programmed or selected (Figure 2).

POWER FACTOR SECTION

The ML4835 power factor section is a peak or average current sensing boost mode PFC control circuit in which only voltage loop compensation is needed. It is simpler than a conventional average current control method. It consists of a voltage error amplifier, a current sense amplifier (no compensation is needed), an integrator, a comparator, and a logic control block. In the boost topology, power factor correction is achieved by sensing the output voltage and the current flowing through the current sense resistor. Duty cycle control is achieved by comparing the integrated voltage signal of the error amplifier and the voltage across R_{SENSE}. The duty cycle control timing is shown in Figure 3.

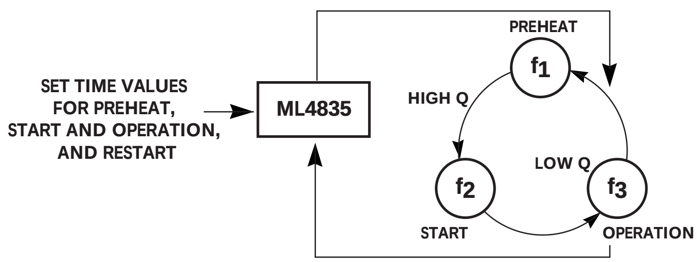


Figure 2. Three Frequency Design Model

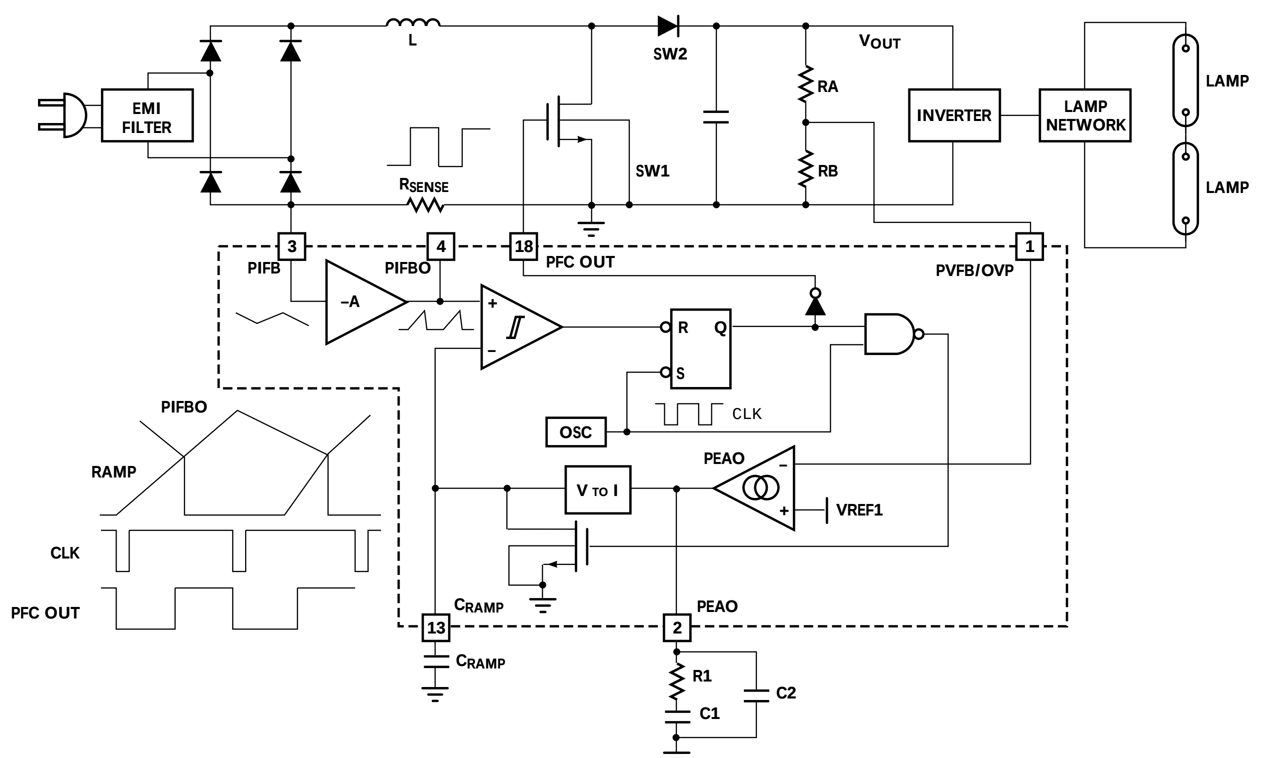


Figure 3. ML4835 PFC Controller Section

FUNCTIONAL DESCRIPTION (Continued)

Setting minimum input voltage for output regulation can be achieved by selecting C_{RAMP} as follows for peak current mode:

$$C_{RAMP} = \frac{PEAO_{MAX}}{22K}\{(1-D)Ts - \Delta t\}\frac{1}{\left[\frac{\sqrt{2}P_{OUT}}{V_{IN}} - \left(\frac{V_{OUT} - \sqrt{2}V_{IN}}{2L}\right)(1-D)Ts\right]8 \times R_{SENSE}} \quad (1)$$

And for average current mode:

$$C_{RAMP} = \frac{PEAO_{MAX}}{22K}\{(1-D)Ts - \Delta t\}\frac{1}{\left[\frac{\sqrt{2}P_{OUT}}{V_{IN}} - \left(\frac{V_{OUT}}{2L}\right)(1-D)Ts\right]8 \times R_{SENSE}} \quad (1a)$$

Where Δt is the dead time.

OVERVOLTAGE PROTECTION AND INHIBIT

The OVP pin serves to protect the power circuit from being subjected to excessive voltages if the load should change suddenly (lamp removal). A divider from the high voltage DC bus sets the OVP trip level. When the voltage on PVFB/OVP exceeds 2.75V, the PFC transistor are inhibited. The ballast section will continue to operate.

TRANSCONDUCTANCE AMPLIFIERS

The PFC voltage feedback amplifier is implemented as an operational transconductance amplifier. It is designed to have low small signal forward transconductance such that a large value of load resistor (R1) and a low value ceramic capacitor (<1μF) can be used for AC coupling

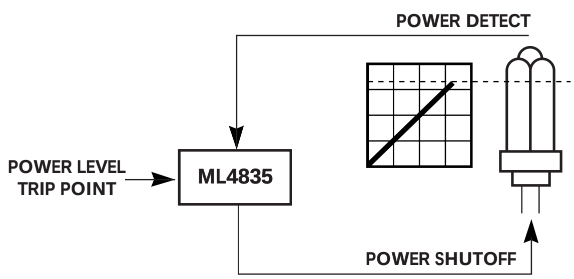


Figure 4. Simplified Model of ML4835 EOL Functionality

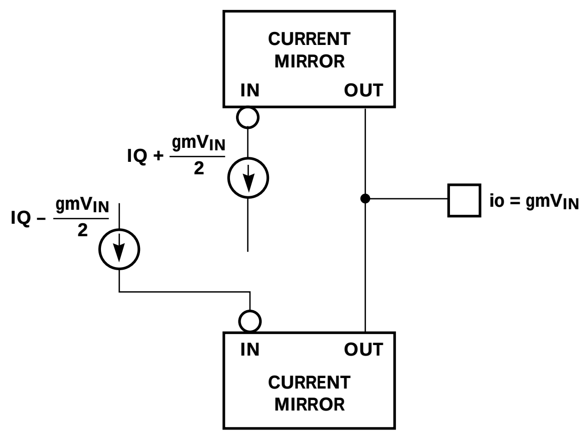


Figure 6. Output Configuration

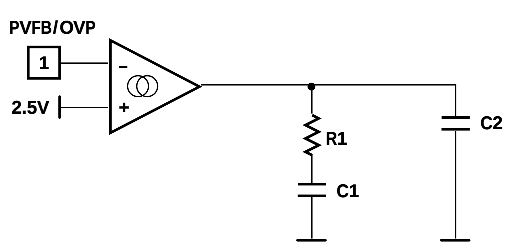


Figure 5. Compensation Network

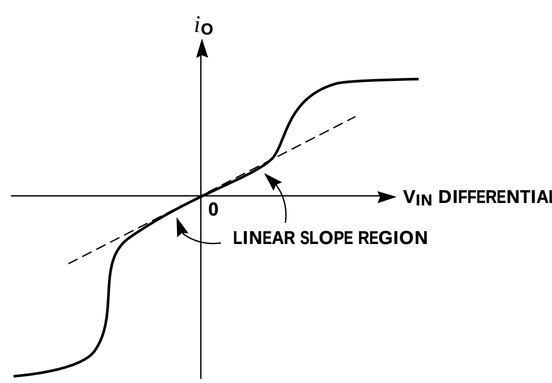


Figure 7. Transconductance Amplifier Characteristics

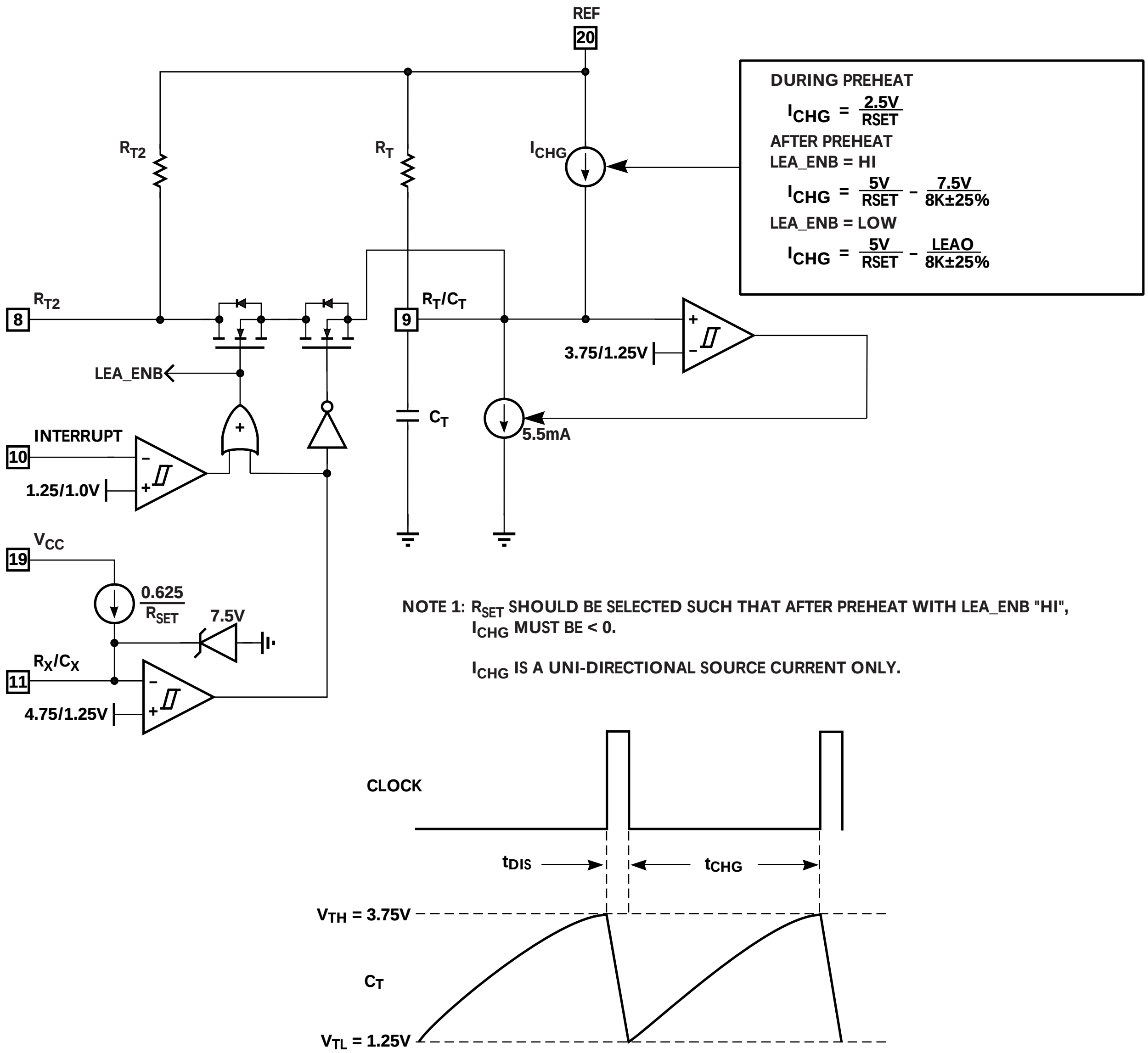


Figure 8. Oscillator Block Diagram and Timing

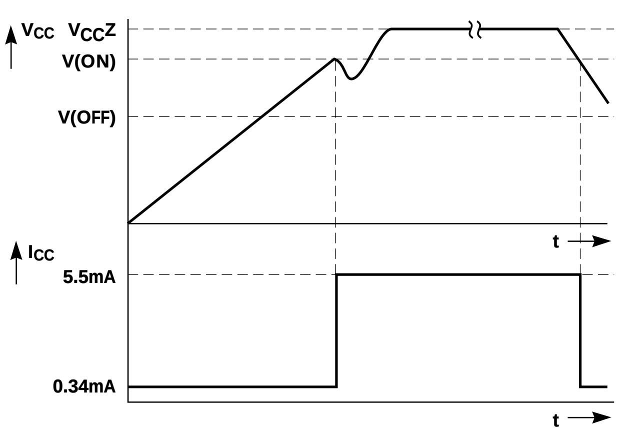


Figure 9. Typical V_{CC} and I_{CC} Waveforms when the ML4835 is Started with a Bleed Resistor from the Rectified AC Line and Bootstrapped from an Auxiliary Winding.

FUNCTIONAL DESCRIPTION (Continued)

(C1) in the frequency compensation network. The compensation network shown in Figure 5 will introduce a zero and a pole at:

$$f_Z = \frac{1}{2\pi R_1 C_1} \qquad f_P = \frac{1}{2\pi R_1 C_2} \tag{2}$$

Figure 4 shows the output configuration for the operational transconductance amplifiers.

A DC path to ground or V_{CC} at the output of the transconductance amplifiers will introduce an offset error. The magnitude of the offset voltage that will appear at the input is given by V_{OS} = io/gm. For an io of 1µA and a gm of 0.05 µW the input referred offset will be 20mV. Capacitor C1 as shown in Figure 5 is used to block the DC current to minimize the adverse effect of offsets.

Slew rate enhancement is incorporated into all of the operational transconductance amplifiers in the ML4835. This improves the recovery of the circuit in response to power up and transient conditions. The response to large signals will be somewhat non-linear as the transconductance amplifiers change from their low to high transconductance mode, as illustrated in Figure 7.

END OF LAMP LIFE

At the end of a lamp's life when the emissive material is depleted, the arc current is rectified and high voltage occurs across the lamp near the depleted cathode. The ballast acts as a constant current source so power is dissipated near the depleted cathode which can lead to arcing and bulb cracking. Compact fluorescent lamps are more prone to cracking or shattering because their small diameter can't dissipate as much heat as the larger linear lamps. Compact fluorescents also present more of a safety hazard since they are usually used in downlighting systems without reflector covers.

EOL and the ML4835

The ML4835 uses a circuit that creates a DC voltage representative of the power supplied to the lamps through the inverter. This voltage is used by the ML4835 to latch off the ballast when it exceeds an internal threshold. An external resistor can be used as the "EOL latch resistor" to set the power level trip point, as shown in by R9 in Figure 12. See Micro Linear ML4835 User Guide and applications notes for more details. Figure 4 illustrates a simplified model of ML4835 EOL functionality.

BALLAST OUTPUT SECTION

The IC controls output power to the lamps via frequency modulation with non-overlapping conduction. This means that both ballast output drivers will be low during the discharging time t_{DIS} of the oscillator capacitor C_T.

OSCILLATOR

The VCO frequency ranges are controlled by the output of the LFB amplifier (R_{SET}). As lamp current decreases, LFB OUT falls in voltage, causing the C_T charging current to increase, thereby causing the oscillator frequency to increase. Since the ballast output network attenuates high frequencies, the power to the lamp will be decreased. The oscillator frequency is determined by the following equations:

$$F_{OSC} = \frac{1}{t_{CHG} + t_{DIS}} \tag{3}$$

and

$$t_{CHG} = R_T C_T \ln\left(\frac{V_{REF} + I_{CHG} \times R_T - V_{TL}}{V_{REF} + II_{CHG} \times R_T - V_{TH}}\right) \tag{4}$$

The oscillator's minimum frequency is set when $I_{CHG} = 0$ where:

$$F_{MIN} \cong \frac{1}{0.51 \times R_T C_T} \tag{5}$$

The oscillator's start frequency can be expressed by:

$$F_{START} = \frac{1}{0.51 \times \left(R_T \| R_{T2}\right) \times C_T} \tag{5a}$$

Both equations assume that $t_{CHG} >> t_{DIS}$.

When LFB OUT is high, $I_{CHG} = 0$ and the minimum frequency occurs. The charging current varies according to two control inputs to the oscillator:

1. The output of the preheat timer
2. The voltage at LFB OUT (lamp feedback amplifier output)

In preheat condition, charging current is fixed at

$$I_{CHG(PREHEAT)} = \frac{2.5}{R_{SET}} \tag{6}$$

In running mode, charging current decreases as the voltage rises from 0V to V_{OH} at the LAMP FB amplifier.

The charging current behavior can be expressed as:

$$I_{CHG} = \frac{5V}{R_{SET}} - \frac{LEAO}{8k \pm 25\%} \tag{7}$$

The highest frequency is attained when I_{CHG} is highest, which is attained when voltage at LFB OUT is at 0V:

$$I_{CHG(0)} = \frac{5}{R_{SET}} \tag{8}$$

FUNCTIONAL DESCRIPTION (Continued)

Highest lamp power, and lowest output frequency are attained when voltage at LFB OUT is at its maximum output voltage (V_{OH}).

In this condition, the minimum operating frequency of the ballast is set per equation 5 above.

For the IC to be used effectively in dimming ballasts with higher Q output networks a larger C_T value and lower R_T value can be used, to yield a smaller frequency excursion over the control range (voltage at LFB OUT). The discharge current is set to 5.5mA.

Assuming that $I_{DIS} >> I_{RT}$:

$$t_{DIS(VCO)} \cong 600 \times C_T \qquad (9)$$

IC BIAS, UNDER-VOLTAGE LOCKOUT AND THERMAL SHUTDOWN

The IC includes a shunt clamp which will limit the voltage at V_{CC} to 15V (V_{CCZ}). The IC should be fed with a current limited source, typically derived from the ballast transformer auxiliary winding. When V_{CC} is below V_{CCZ} – 1.1V, the IC draws less than 0.55mA of quiescent current and the outputs are off. This allows the IC to start using a “bleed resistor” from the rectified AC line.

To help reduce ballast cost, the ML4835 includes a temperature sensor which will inhibit ballast operation if the IC’s junction temperature exceeds 130°C. In order to use this sensor in lieu of an external sensor, care should be taken when placing the IC to ensure that it is sensing temperature at the physically appropriate point in the ballast. The ML4835’s die temperature can be estimated with the following equation:

$$T_J \cong T_A + (P_D + 65°C / W) \qquad (10)$$

STARTING, RE-START, PREHEAT AND INTERRUPT

The lamp starting scenario implemented in the ML4835 is designed to maximize lamp life and minimize ballast heating during lamp out conditions.

The circuit in Figure 10 controls the lamp starting scenarios: Filament preheat and lamp out interrupt. C_X is charged with a current of $I_{R(SET)}/4$ and discharged through R_X. The voltage at C_X is initialized to 0.7V (V_{BE}) at power up. The time for C_X to rise to 4.75V is the filament preheat time. During that time, the oscillator charging current (I_{CHG}) is 2.5/R_{SET}. This will produce a high frequency for filament preheat, but will not produce sufficient voltage to ignite the lamp or cause significant glow current.

After cathode heating, the inverter frequency drops to F_{START} causing a high voltage to appear to ignite the lamp. If lamp current is not detected when the lamp is supposed to have ignited, the C_X charging current is shut off and the inverter is inhibited until C_X is discharged by R_X to the 1.25V threshold. Shutting off the inverter in this manner prevents the inverter from generating excessive heat when the lamp fails to strike or is out of socket. Typically this time is set to be fairly long by choosing a large value of R_X.

LFB OUT is ignored by the oscillator until INTERRUPT is above 1.25V The C_X pin is clamped to about 7.5V.

Care should also be taken not to turn on the VCCZ clamp so as not to dissipate excessive power in the IC. This will cause the temp sensor to become active at a lower ambient temperature.

A summary of the operating frequencies in the various operating modes is shown below.

OPERATING MODE	OPERATING FREQUENCY
Preheat	$\frac{[F(MAX) \text{ to } F(MIN)]}{2}$
After Preheat	F(START)
Dimming Control	F(MIN) to F(MAX)

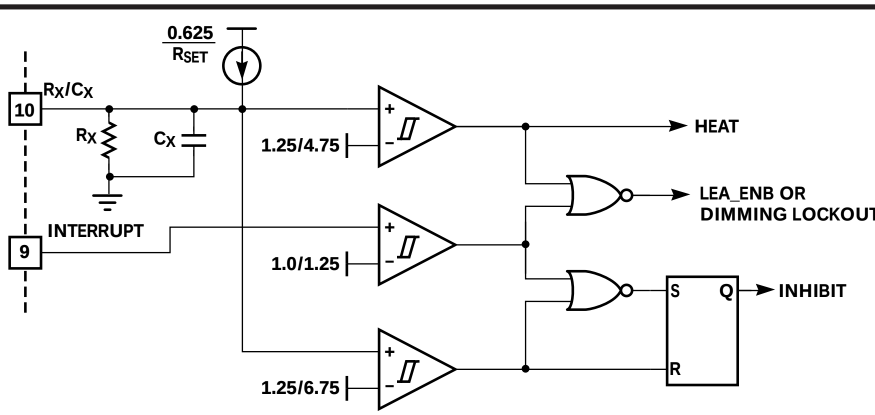


Figure 10. Lamp Preheat and Interrupt Timers

TYPICAL APPLICATIONS

The ML4835 can be used for a variety of lamp types:

- T4 or compact fluorescent lamps
- IEC T8 (linear lamps)
- T5 linear lamps
- T12 linear lamps

The ML4835 can also be used for dimming applications. For example, 20:1 dimming can be achieved using the ML4835 with external dimming units. The applications schematics shown in Figures 12, 13, and 14 are examples of the various uses of the ML4835.

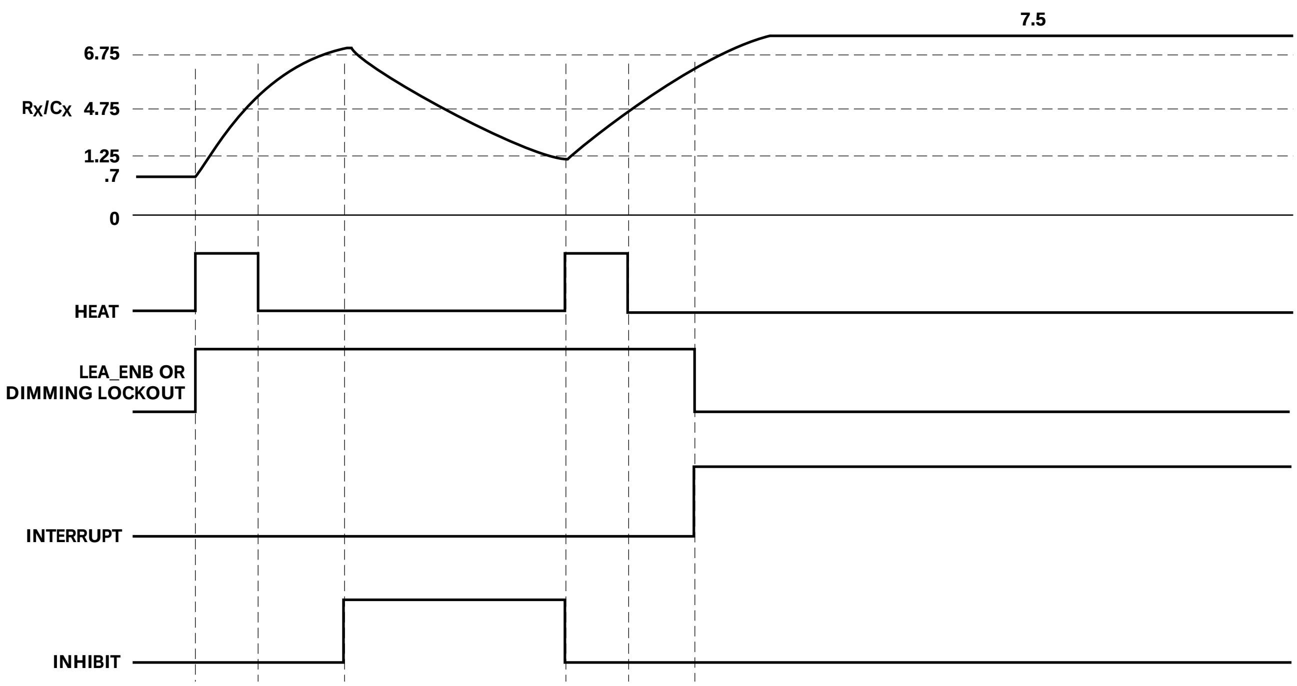


Figure11. Lamp Starting and Restart Timing

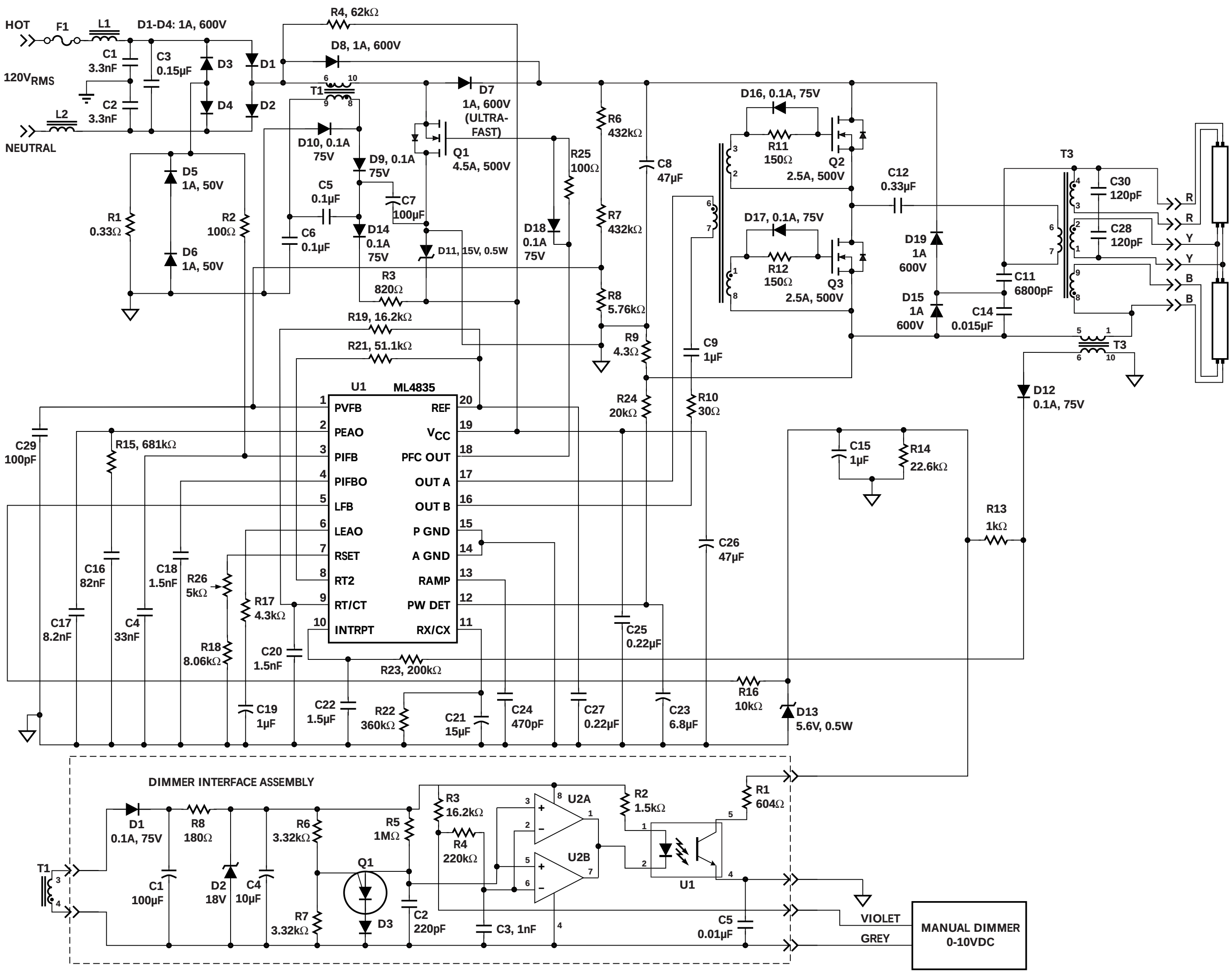


Figure12. Ballast for Architectural Dimming Applications

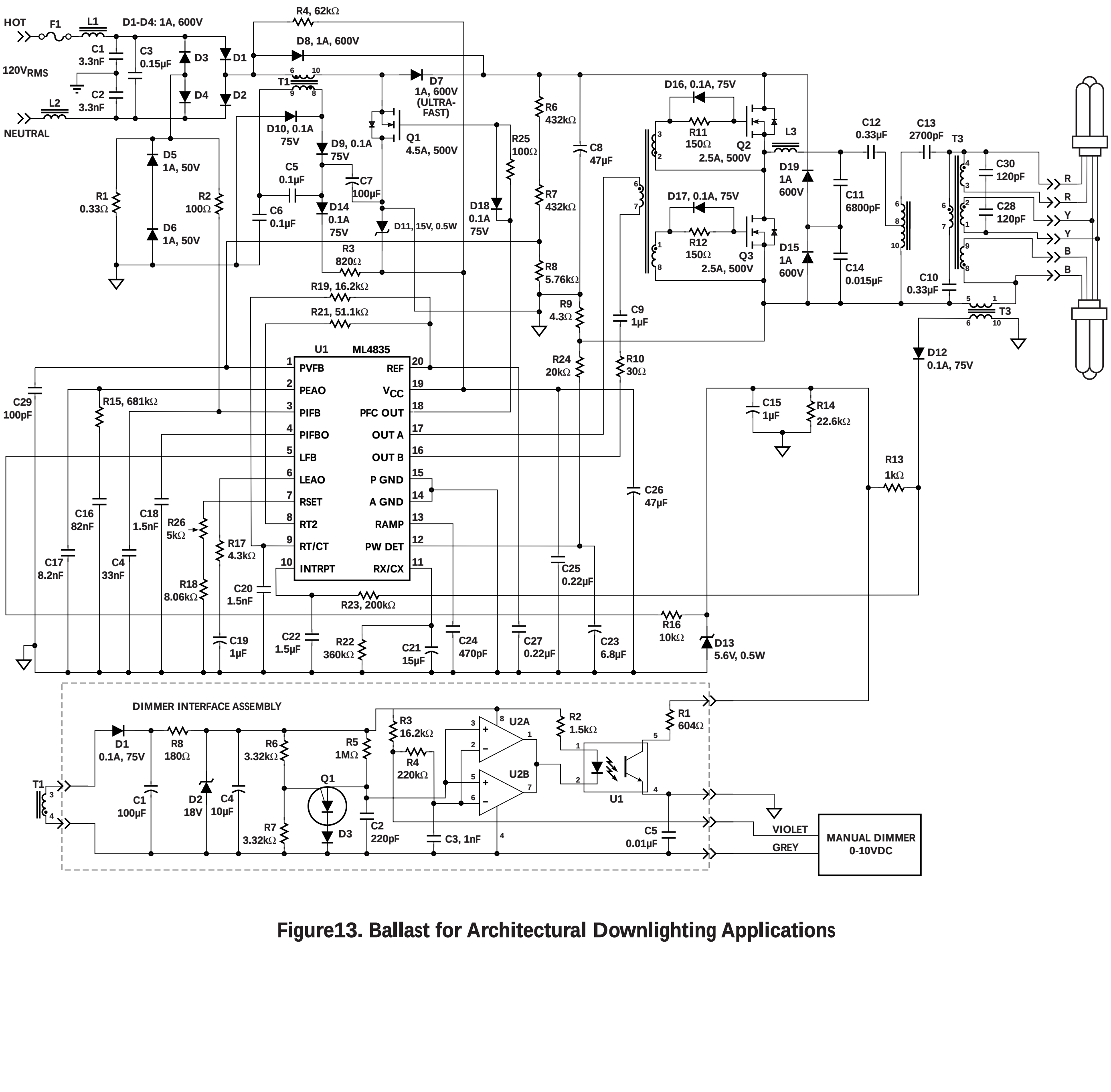


Figure13. Ballast for Architectural Downlighting Applications

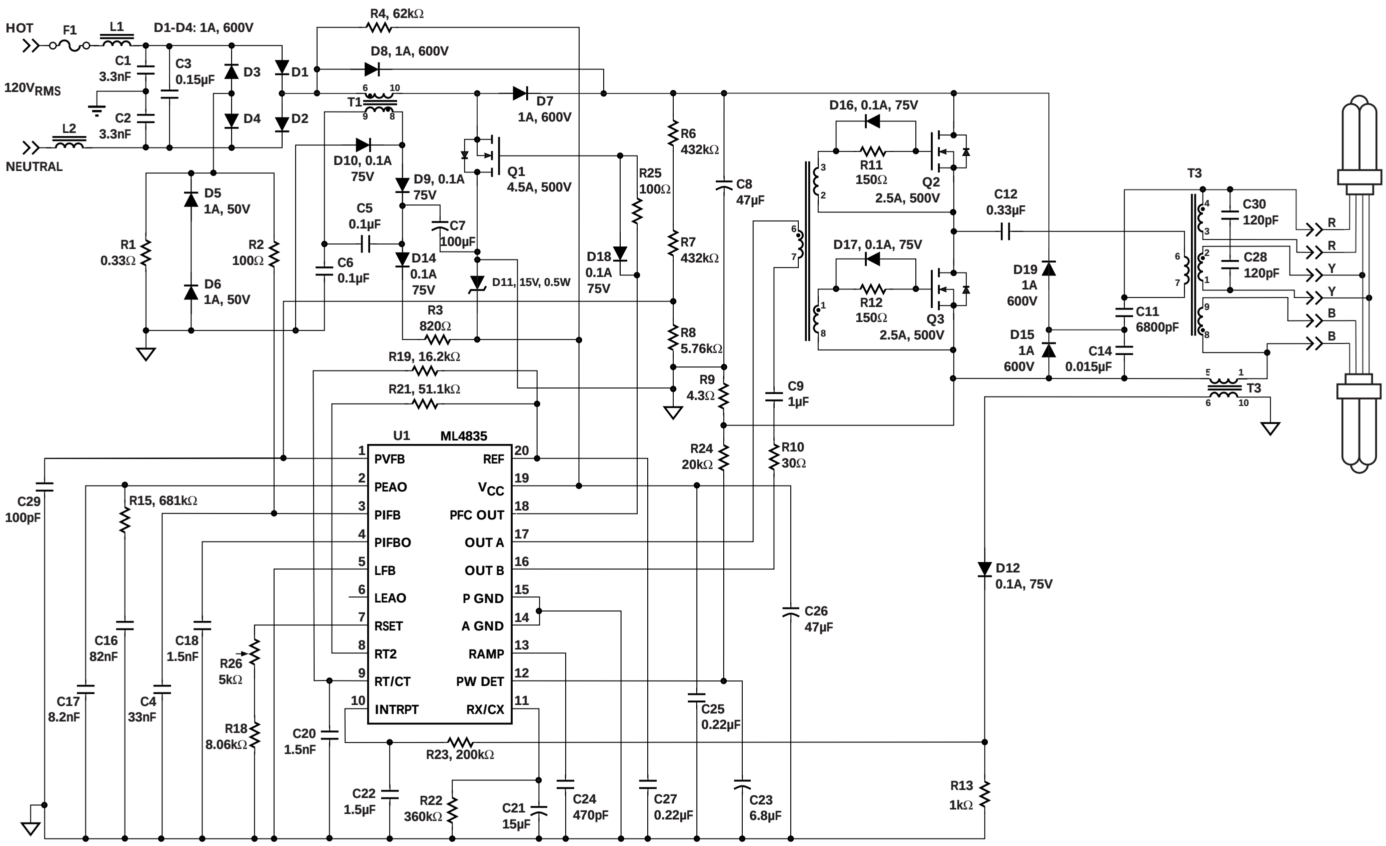
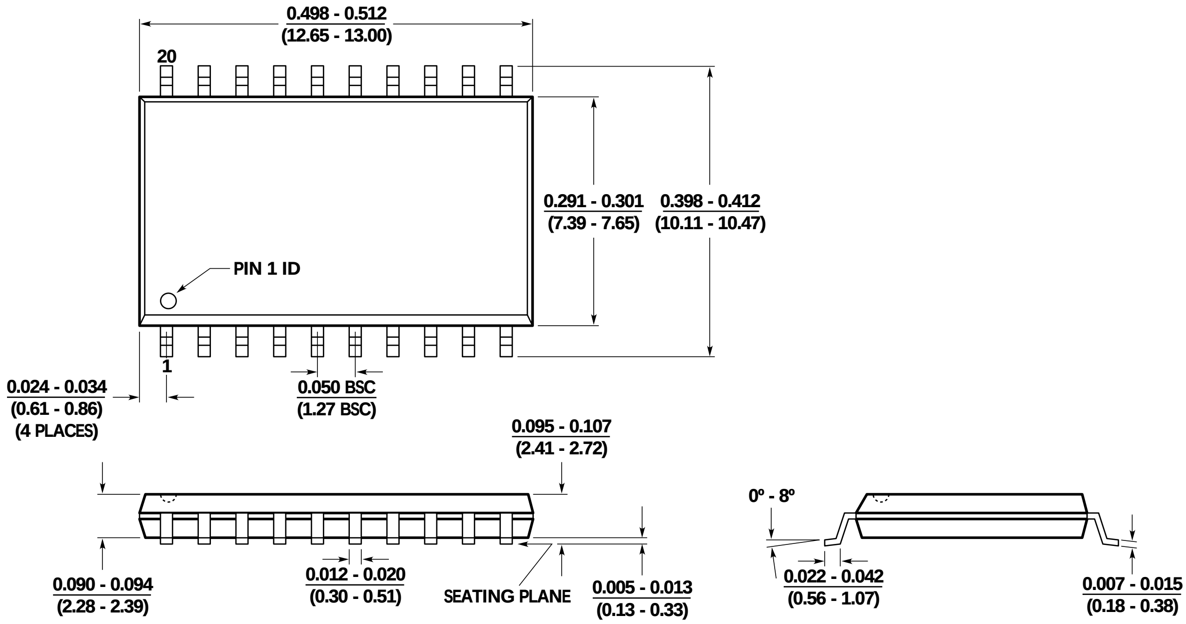


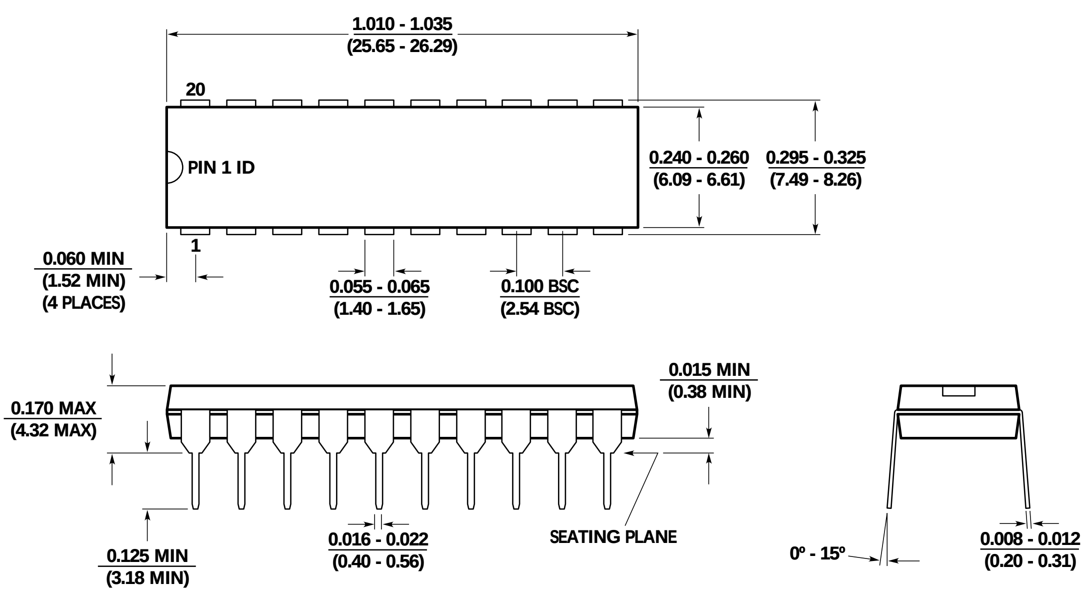
Figure14. Non-Dimming Ballast for Downlighting Applications

PHYSICAL DIMENSIONS inches (millimeters)

Package: S20
20-Pin SOIC



Package: P20
20-Pin PDIP



ORDERING INFORMATION

PART NUMBER	TEMPERATURE RANGE	PACKAGE
ML4835CP (End Of Life) ML4835CS (End Of Life)	0°C to 70°C 0°C to 70°C	20-Pin DIP (P20) 20-Pin SOIC (S20)